

IMEC's GaN-IC technology

GaN-on-SOI and GaN-on-QST®

Private lots

Both, imec's GaN-on-SOI and GaN-on-QST® technologies are available through private lots.

The offer includes:

- 12 full 8-inch wafers with your devices
- The lot is processed as soon as the mask is received.
- Lead time is ~19 weeks as a DRC clear GDS is received.
- Price estimation for GaN-on-SOI is 270Keuro
 - (250Keuro for the lot +20K for the mask)
- Price estimation for GaN-on-QST® is 320Keuro
 - (300Keuro for the lot +20K for the mask)

MPW (Multi-Project-Wafer service)

In particular, only the GaN-on-SOI (GaN-IC) technology is available through imec's MWP services. Please find below information about schedules and prices on 2021 MPW shuttles.

Europractice GaN-IC MPW run – Schedule 2021

2021 imec GaN-IC on SOI run schedule:

| imec | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
|-------------------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| imec GaN-IC on SOI 200V | | | | | 19 | | | | | | | |
| imec GaN-IC on SOI 650V | 20 | | | | | | | | | 20 | | |

Dates indicate deadlines for both **Registration** and submission of the **first version of the GDS** file.

To allow for a smooth tape-in, all imec GaN designs have to go through a weekly checkpoint in the weeks that follow the registration deadline.

In particular, this means that a first version of the design is required by RD + 0 weeks.

Europractice strives to send feedback on that design by RD+1 week.

A second, corrected version of the design is needed by RD + 2 weeks.

Feedback on the second version will be sent by RD + 3 weeks.

The final, DRC-clear design is expected by RD + 4 weeks.

Every design that is submitted by the user should submit with the suggested DRC results.

Europractice GaN-IC MPW run – Price list 2021

General conditions:

- Prices and conditions may change at any time without prior notice.
- Depending on run participation, or the number of submissions, the schedule can be adapted or runs may be cancelled. The price can also depend on run participation for some technologies.
- Prices are in EURO
- Prices are for the delivery of unpackaged, untested prototypes

Technology specific pricing:

2021 GaN Power Electronics pricing

imec GaN-IC on SOI 200V and 650V

| Options | Standard EUR / block | Discounted EUR / block |
|---------------------------------|-------------------------|---------------------------|
| Area = 2.5mm x 5.18mm* | 17,000 | 16,150 |
| Area = 5.18mm x 5.18mm | 33,000 | 31,350 |
| Area = 10.54mm x 5.18mm | 66,000 | 62,700 |
| Extra Options | | |
| Extra set of chips (40 samples) | +5,000 | +5,000 |

Important notes: Regular number of samples is 40 (Starting June 2021).

Due to the nature of MPW logistics, more chips than ordered may sometimes be shipped.

*This option is only available to academic institutions.

EU-Discounted Price applies to EURO PRACTICE registered (who paid their annual full membership fee) Academic and Research Members from all 28 EU countries and Albania, Armenia, Azerbaijan, Belarus, Bosnia-Herzegovina, Georgia, Iceland, Israel, Liechtenstein, Former Yugoslav Republic of Macedonia, Moldova, Montenegro, Norway, Russia, Switzerland, Turkey, Serbia and Ukraine who submit designs for educational or publicly funded research use only.

Standard Price applies to universities and research institutes that do not belong to the previous category. Companies can contact the foundry directly or can be served through Europractice but approval from the foundry is required.

Disclaimer The listed Europractice prices (EU-discounted as well as standard prices) are for fabrication of a set of prototype samples of the designs (GDS) of the customer on MPW runs in standard GaN-IC technologies. For any change in technology (options, process tuning, etc.) or extra samples/wafers, dedicated mask sets and engineering runs, customers need to request a dedicated quote.

Contact ganmpw@imec-int.com